

1/5

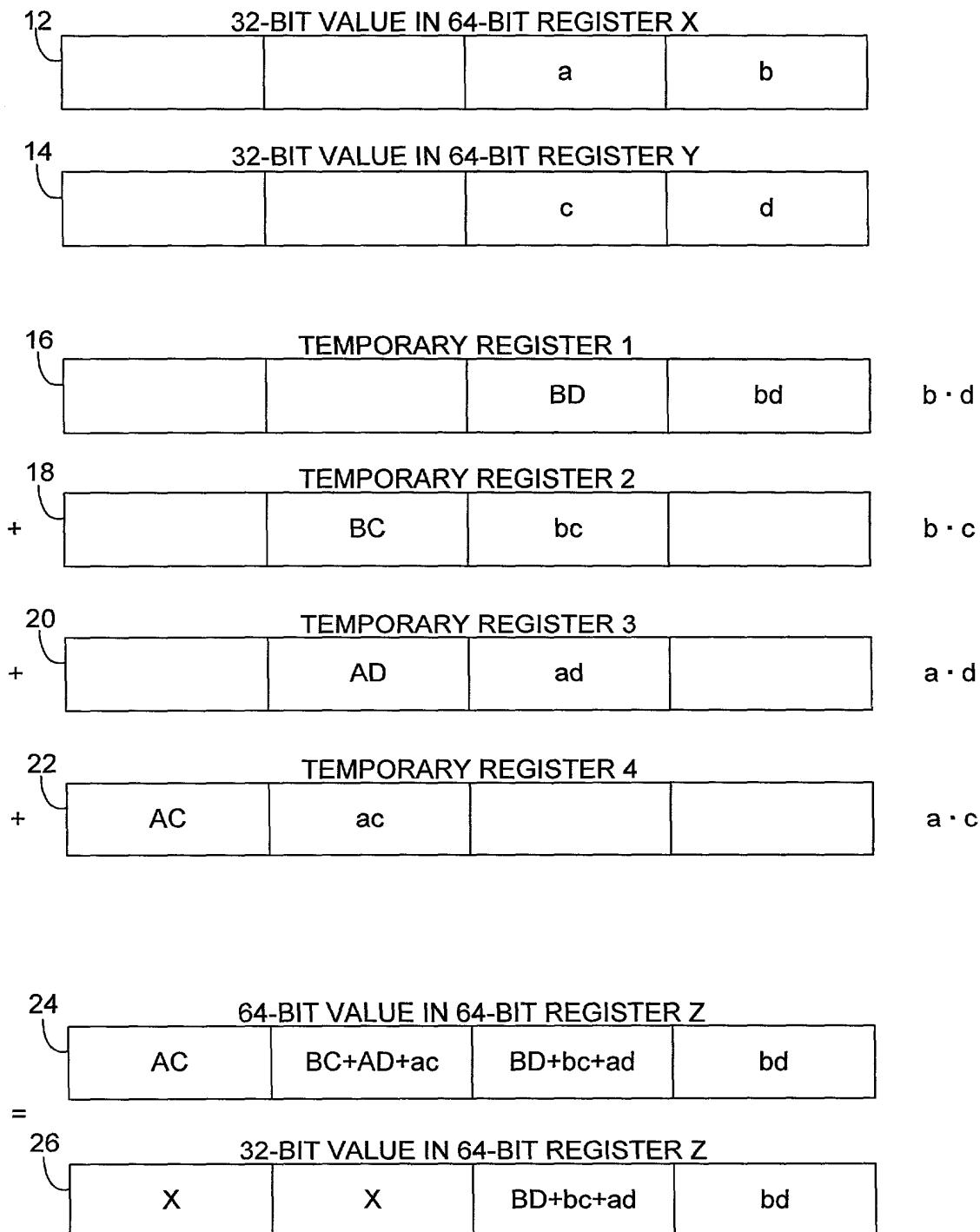
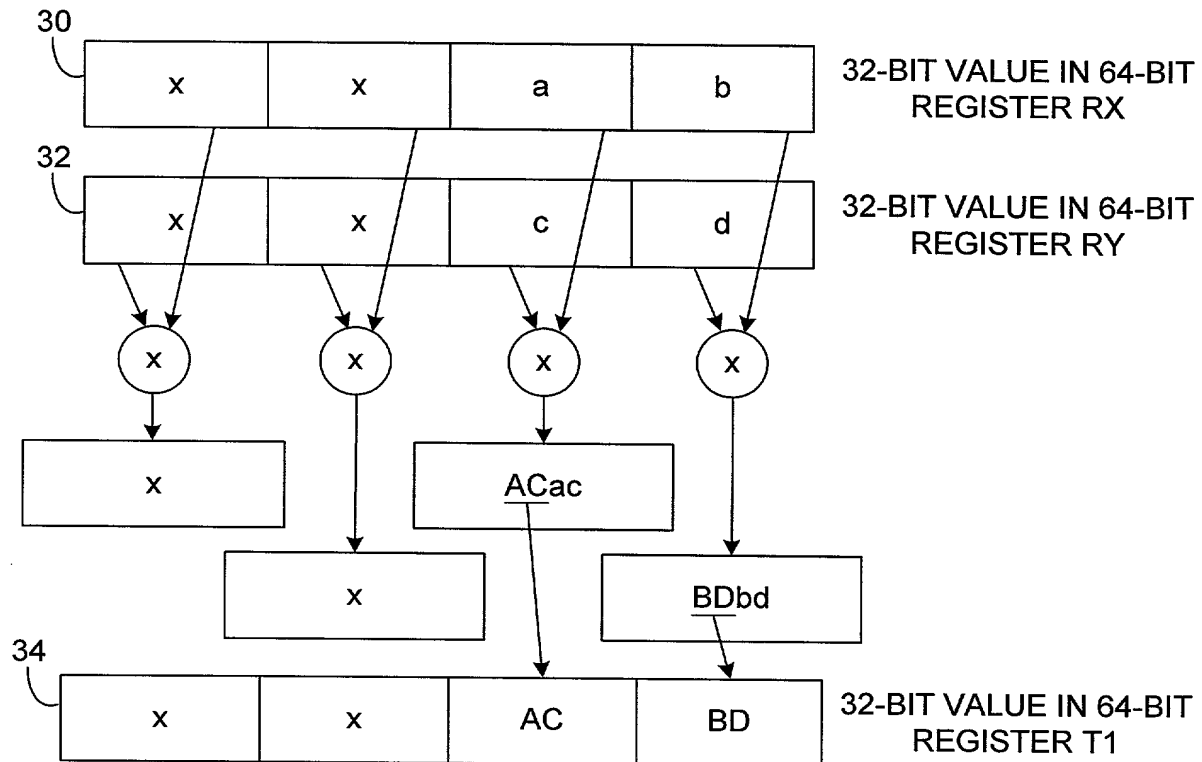


FIG. 1  
(PRIOR ART)

2/5

1: PMPYSHR2.U T1=RX,RY,16



2: MUX2 T2=RX,0XE0

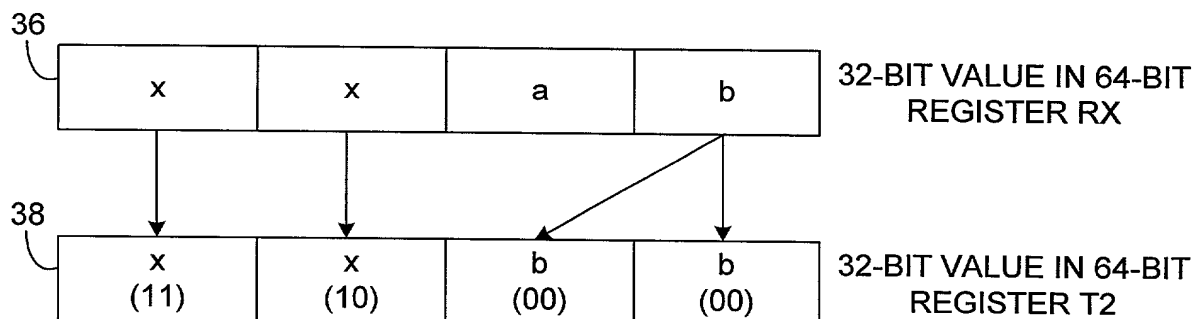
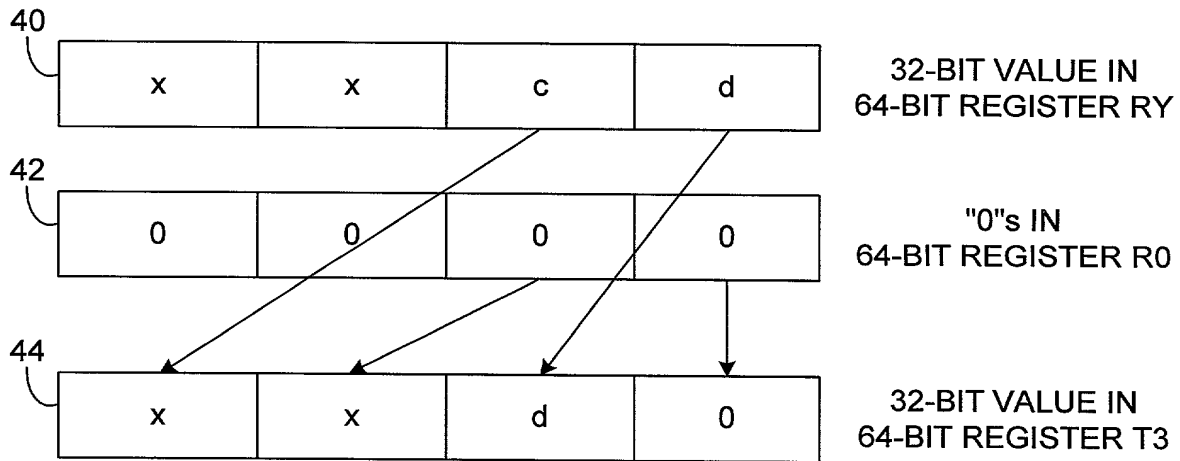


FIG. 2A

28

3/5

3: UNPACK2.L T3=RY,R0



4: PMPYSHR2.U T4=T2,RY,0

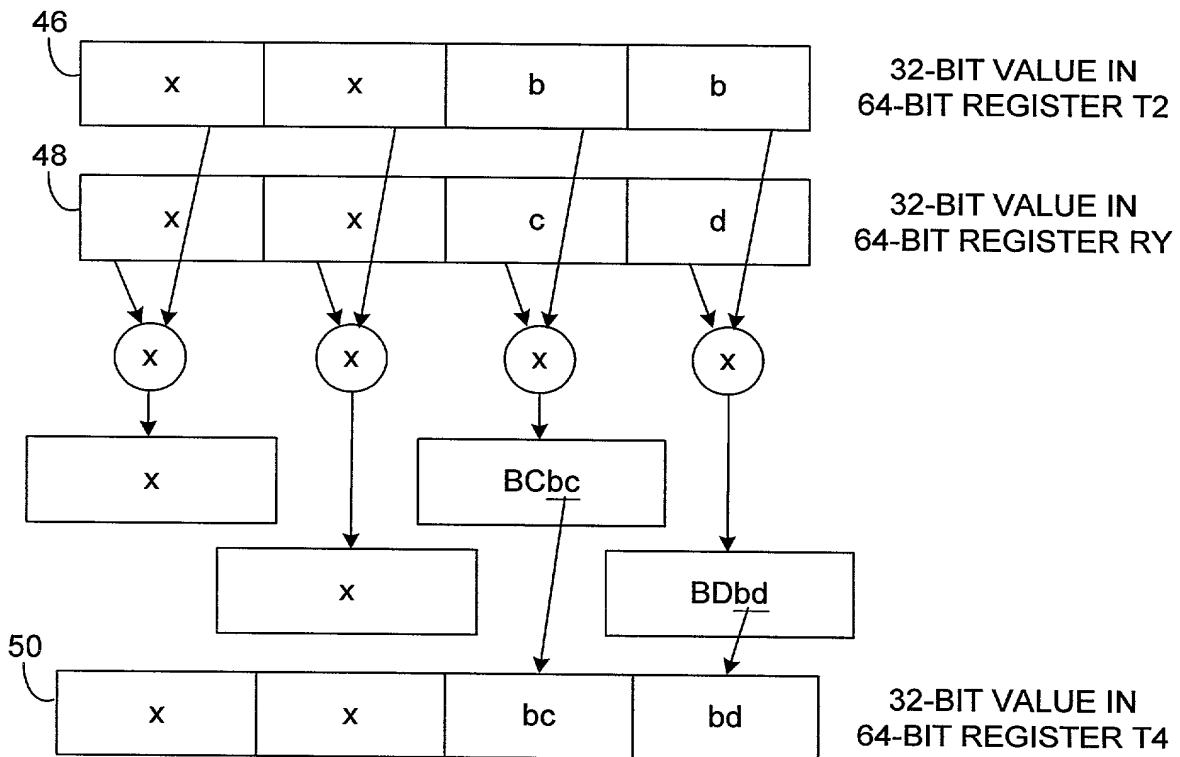
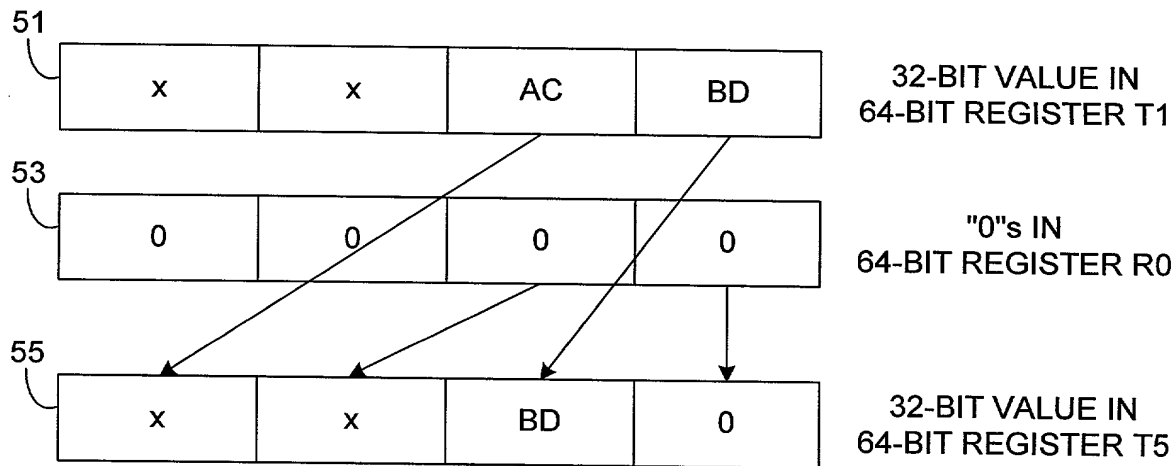


FIG. 2B

4/5

5: UNPACK2.L T3=RY,R0



6: PMPYSHR2.U T6=RX,T3,0

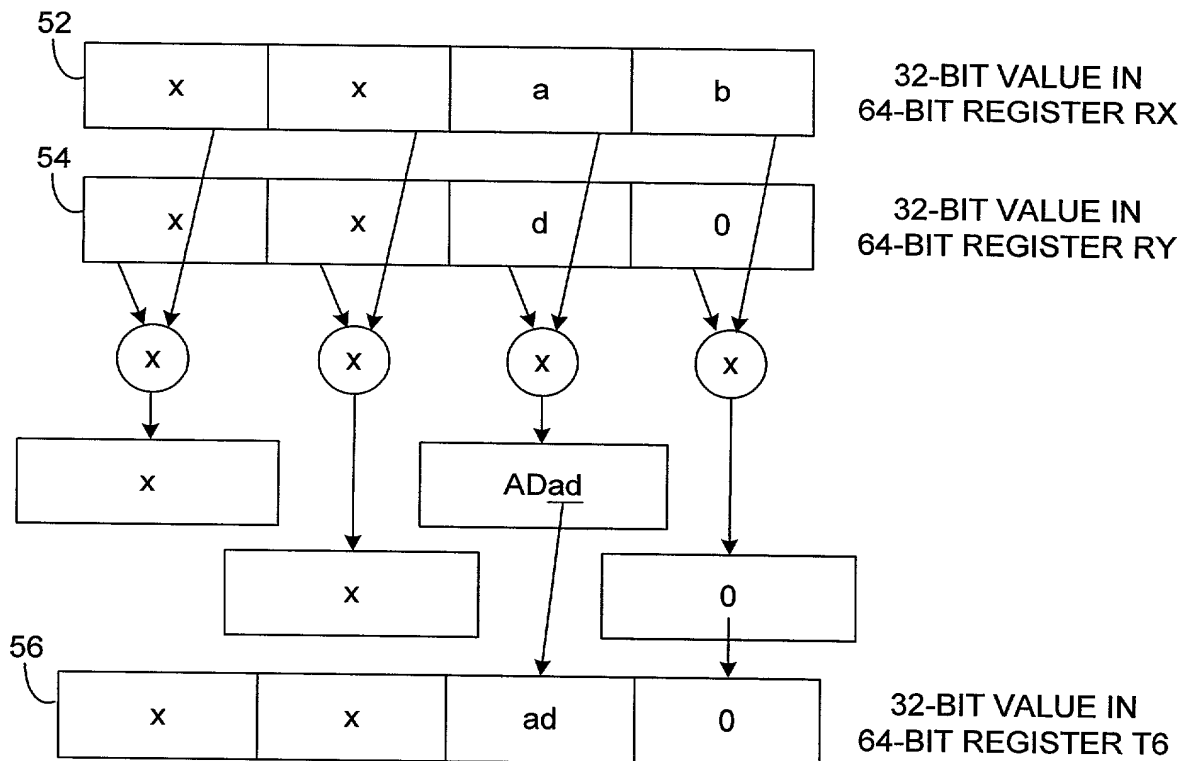
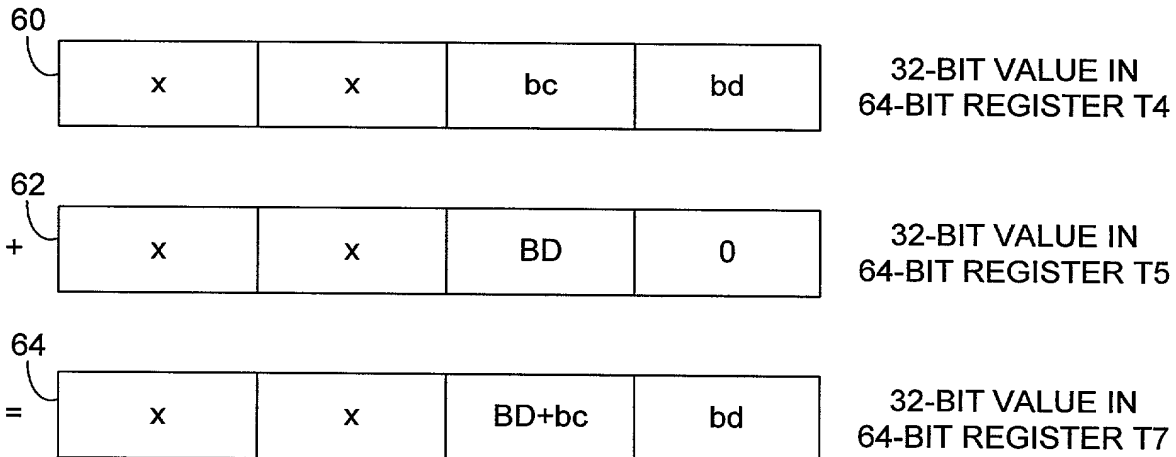


FIG. 2C

7: ADD

T7=T4,T5



8: ADD

RZ=T6,T7

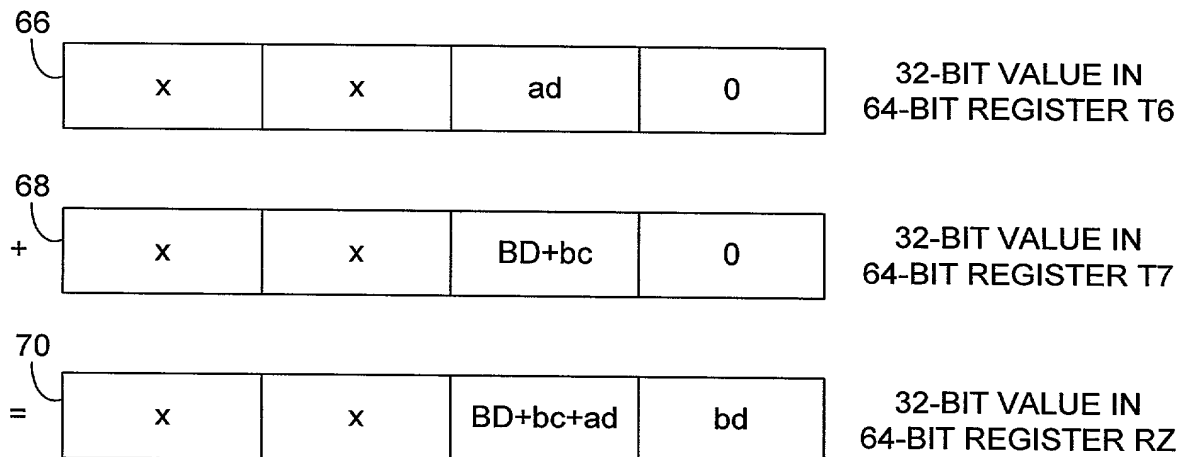


FIG. 2D